PATENT COOPERATION TREATY

PCT

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

(Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

Appl	Icant's or agent's file referer	FOR FURTHER	ACTION	See Form PCT/PEA/416	
ABE	3 55PCT	3,11 3111121			
International application No. PCT/FI2004/000289		International filing da 13.05.2004	te (day/month/year)	Priority date (day/month/year) 15.05.2003	
International Patent Classification (IPC) or national classification and IPC H02H3/253					
	licant B OY et al.				
1.	This report is the interr	national preliminary examination 35 and transmitted to the applic	report, established by	r this International Preliminary Examining e 36.	
2.	This REPORT consists	s of a total of 5 sheets, including	g this cover sheet.		
3.	This report is also acco	ompanied by ANNEXES, compr	ising:		
		licant and to the International Bo			
	and/or shee	ne description, claims and/or dra ets containing rectifications auth tive Instructions).	wings which have bee orized by this Authorit	en amended and are the basis of this report y (see Rule 70.16 and Section 607 of the	
	sheets which supplements supplements.	disclosure in the international a	t which this Authority c application as filed, as	onsiders contain an amendment that goes indicated in item 4 of Box No. I and the	
	seguence listing	ernational Bureau only) a total o g and/or tables related thereto, i Sequence Listing (see Section	n computer readable f	mber of electronic carrier(s)) , containing a orm only, as indicated in the Supplemental tive Instructions).	
4.	This report contains in	dications relating to the followin	g items:	7	
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		s of the opinion			
	☐ Box No. II Prìor	•	agerd to povelty inven	tive step and industrial applicability	
		of unity of invention	sgura to novelty, invest	ave stop and massaca approach,	
			5(2) with regard to no	velty, inventive step or industrial	
	appl	icability; citations and explanation	ons supporting such st	atement	
		ain documents cited		·	
		ain defects in the international a			
	☐ Box No. VIII Cert	ain observations on the internat	ional application		
<u></u>			Date of completion	of this report	
Date	e of submission of the dema	ina	Date of completion	Or this report	
14.	03.2005		27.09.2005		
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INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No. PCT/FI2004/000289

_	Box No. I Basis of the repor					
1.	With regard to the language, th filed, unless otherwise indicated	th regard to the language , this report is based on the international application in the language in which it was ed, unless otherwise indicated under this item.				
	This report is based on tran which is the language of a	nslations from the original language into the following language , translation furnished for the purposes of:				
	D publication of the internal	der Rules 12.3 and 23.1(b)) ational application (under Rule 12.4) examination (under Rules 55.2 and/or 55.3)				
2.	With regard to the elements* of the international application, this report is based on (replacement sheets have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in treport as "originally filed" and are not annexed to this report):					
	Description, Pages					
	1, 2, 4-9	as published				
	3	filed with telefax on 14.03.2005				
	Claims, Numbers					
	1-9	filed with telefax on 14.03.2005				
	Drawings, Sheets					
	1/3-3/3	as published				
	☐ a sequence listing and/or a	ny related table(s) - see Supplemental Box Relating to Sequence Listing				
3.	☐ The amendments have res	☐ The amendments have resulted in the cancellation of:				
	☐ the description, pages☐ the claims, Nos.					
	the drawings, sheets/fig:	S				
	☐ the sequence listing (sp☐ any table(s) related to s					
	any table(s) related to s	equence isting (specify).				
4.	☐ This report has been estab had not been made, since they Supplemental Box (Rule 70.2(c)	lished as if (some of) the amendments annexed to this report and listed below have been considered to go beyond the disclosure as filed, as indicated in the)).				
	the description, pagesthe claims, Nos.					
	the drawings, sheets/fig					
	☐ the sequence listing (sp☐ any table(s) related to s					
		ome or all of these sheets may be marked "superseded."				
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INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No. PCT/FI2004/000289

Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

1. Statement

Novelty (N)

Yes: Claims

1-9

No:

Inventive step (IS)

Yes: Claims

Claims

1-9

No: Claims

Industrial applicability (IA)

Yes: Claims

1-9

No: Claims

2. Citations and explanations (Rule 70.7):

see separate sheet

Box No. VII Certain defects in the international application

The following defects in the form or contents of the international application have been noted:

see separate sheet

Re Item V

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Reasoned statement with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

1. Reference is made to the following documents:

point and the second connection point.

D1: US-A-3 999 087 (COMPTON J R) 21 Dec. 1976

D2: US-A-4 027 204 (NORBECK D K) 31 May 1977

D3: US-A-3 001 100 (SCHUH N F ET AL) 19 Sept. 1961

2. The document D1 is regarded as being the closest prior art to the subject-matter of independent claim 1, and shows a phase failure detector for a multi-phase electricity supply network, according to the preamble of present claim 1. Particularly, D1 teaches a phase failure detector including a detector circuit (21A,21B,21C) for each phase, which detector circuit has a first connection point for connecting to the phase (φ_A,φ_B,φ_C) being monitored and a second connection point, which is connected to a common connection point (N) of the detector circuits, in which each detector circuit includes voltage divider elements (22A-22C,24A-24C,26A-26C) for dividing voltage between the first and the second connection point and for feeding reduced voltage to an input point (cathodes of 26A-26C), and a trigger and detector circuit (16A-16C) connected between the reduced voltage input

- 2.1 The subject-matter of claim 1 differs from this known phase failure detector in that
 - i) the voltage divider elements include at least two capacitive elements, which participate in the division of the voltage and of which at least one is arranged to store energy and to discharge the energy it stores through the trigger and detector circuit, and
 - ii) each trigger and detector circuit is arranged to produce a detection pulse when the reduced voltage reaches a trigger value, whereby the phase failure detector can, in addition to detecting a fault state, also detect which of the phases is defective.

- 2.2 The subject-matter of claim 1 is therefore new (Article 33(2) PCT).
- 2.3 The problem to be solved by the present invention may be regarded as to increase the efficiency and to improve the degree of discrimination capabilities of a phase failure detector according to D1.
- 2.4 The solution to this problem proposed in claim 1 of the present application is considered as involving an inventive step (Article 33(3) PCT) since none of the documents cited in the international search report shows or even suggests the above-mentioned differentiating features i) and ii), neither alone nor in combination.
- 3. Claims 2 to 9 are dependent on claim 1 and as such also meet the requirements of the PCT with respect to novelty and inventive step.
- 4. The industrial applicability (Art. 33(4) PCT) in view of the cited documents is obviously given for the subject-matter of all claims.

Re Item VII

Certain defects in the international application

5. Contrary to the requirements of Rule 5.1(a)(ii) PCT, the relevant background art disclosed in the document D1 is not mentioned in the description, nor is this document identified therein.

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between the phase being monitored and a common connection point, so that the common connection point forms a reference level. This reference level can be floating, in which case it corresponds to a virtual star point of the power supply network. The reference level can also be connected to a real star point, a ground potential, or some other suitable potential. The detector circuit itself includes voltage-divider elements, and a trigger circuit and detector, to which the voltage being monitored is fed in a reduced form from the voltage-divider elements.

More specifically, the phase failure detector according to the invention is characterized by what is stated in the characterizing portion of Claim 1.

The device, according to the invention, utilizing a multi-phase mains power supply is, in turn characterized by what is stated in the characterizing portion of Claim 8.

15 Considerable advantages are gained with the aid of the invention.

With the aid of the invention, it is possible to implement a simple and inexpensive phase failure detector, which can detect not only a fault state, but also which of the phases is defective.

The invention also has numerous embodiments, by means of which significant additional advantages are gained.

In an embodiment, in which the detector circuits are connected to a common reference point with a known potential, the phase failure detector can also detect a fault state, in which all phases are defective.

The invention permits the phase failure detector to be designed with an extremely simple construction. In such a simple embodiment, only a few discrete components are required for the phase failure detector and the manufacturing costs remain very small. The phase failure detector can also be integrated on a circuit board and manufactured with a very small physical size.

AMENDED SHEETS

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Claims:

- 1. A phase failure detector (1) for a multi-phase electricity supply network, which phase failure detector includes a detector circuit (3) for each phase (2), which detector circuit (3) has a first connection point (4) for connecting to the phase (2) being monitored and a second connection point (5), which is connected to a common connection point (9) of the detector circuits, in which each detector circuit (3) includes
- voltage divider elements (6) for dividing voltage between the first (4) and the second (5) connection point and for feeding reduced voltage to an input point (7), and
- a trigger and detector-circuit (8) connected between the reduced voltage input point (7) and the second connection point (5),

characterized in that

- the voltage divider elements (6) include at least two capacitive elements (C1, C2), which participate in the division of the voltage and of which at least one (C2) is arranged to store energy and to discharge the energy it stores through the trigger and detector circuit (8), and
- each trigger and detector circuit (8) is arranged to produce a detection pulse when
 the reduced voltage reaches a trigger value, whereby the phase failure detector can,
 in addition to detecting a fault state, also detect which of the phases is defective.
- 2. A phase failure detector according to Claim 1, characterized in that the phase failure detector includes a resistive element between the capacitive elements (C1, C2) and the first connection point (4).
- 3. A phase failure detector according to Claim 1 or 2, characterized in that the operating energy of the trigger and detector circuit (8) is taken from the voltage divider elements (6).
- 4. A phase failure detector according to any of Claims 1 3, c h a r a c t e r i z e d in that the trigger and detector circuit (8) includes a triggering circuit element (V1, V2, V3), which triggers to a conducting state when the control voltage rises to a specific

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triggering level.

- 5. A phase failure detector according to any of Claims 1 4, characterized in that the trigger and detector circuit (8) includes a rectifier (V4).
- 6. A phase failure detector according to any of Claims 1 5, characterized in that the trigger and detector circuit (8) includes an opto-link (V5).
- 7. A phase failure detector according to any of Claims 1 6, characterized in that it is arranged to be used in a three-phase network, in which case the phase failure detector includes exactly three detector circuits (3).
 - 8. A device utilizing multi-phase network input, characterized in that it includes a phase failure detector (1) according to any of Claims 1 7.
 - 9. A device according to Claim 8, characterized in that it is a rectifier and that the common connection point (9) of the detector circuits of the phase failure detector (1) is connected to a reference potential taken from the direct-voltage circuit of the rectifier.

AMENDED SHEETS